



B/IDW
2133

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Docket No.: CRDC-P0337

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the below date of deposit.

Date of Deposit:	12/28/04	Name of Person Making the Deposit:	Judy Davenport	Signature of the Person Making the Deposit:	<i>Judy Davenport</i>
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Inventor(s): Wajih Dalal, Masashi Shimanouchi, Robert Glenn, and Burnell G. West

Serial No.: 09/679,042 Group Art Unit: 2133

Filed: 10/02/00 Examiner: Chaudry, Mujtaba M.

Confirmation No: 4713

Title: METHOD AND APPARATUS FOR HIGH SPEED IC TEST INTERFACE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

TRANSMITTAL OF FORMAL DRAWINGS

In response to Drawing Informalities

attached please find:

(a) the formal drawings for this application
Number of Sheets 4

Each sheet of drawing indicates the identifying indicia suggested in § 1.84(c) on the reverse side of the drawing

(b) a copy of the NOTICE OF INFORMAL DRAWINGS

Please direct all correspondence concerning the above-identified application to the following address:

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Respectfully submitted,

Date: 12/28/2004

By: 
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